

SMW Dual Output PLL-LNB



The Optimized Low- and High Band system

The Dual Output PLL-LNB is the commercial solution to receive the Low- and High band simultaneously with high LO-stability for applications like Low- to High speed data, Fly-away systems, Digital audio, VSAT and other Commercial applications.

The solution consists of one LNA with dual SMA-outputs, SMA-cables (4-6 m) and two Block Downconverters. One for each frequency range. All parts are optimized, adjusted and tested as a complete matched unit. The LO stability is +/- 25 kHz (option +/- 10 kHz) over temperature to limit the frequency drift. To ensure the lowest possible BER (Bit Error Rate) the Phase noise is optimized to a very low level.

All units are individually hand tuned to get the very best performance available for each unit. Quality and long term reliability is also essential. Therefore are all units tested according to a very extensive test program, which includes heating, cooling, water-proof testing and rigorous electrical testing.

Swedish Microwave was founded 1986 and, within Europe, is the oldest manufacturer of LNBs. In the standard product range we have DRO-LNBs, PLL-LNBs, LNAs, Block Downconverters (BDC), Up- & Down Converters, Quattro LNBs, Twin LNBs, Ortho mode transducers (OMT), Line Amplifiers and Feed horns.

Swedish Microwave is today one of the leading manufacturers of microwave components needed for satellite receiving equipment and other industrial products.

Specification SMW Dual Output PLI -I NB System

SMW L	.O 9.75 & 10.75 GHz	LO 10.0 & 10.75 GHz	LO 10.0 & 11.3 GHz
Frequency range Low / High 1 LO frequency 9	0.7 - 11.8 / 11.7 - 12.75 GHz 0.75 GHz (Low band)	10.95 - 12.1 / 11.7 - 12.75 GHz 10.0 GHz (Low band)	10.95 - 12.1 / 12.25 - 12.75 GHz 10.0 GHz (Low band)
Output frequency 9	0.75 GHz (High band) 950 - 2050 MHz (Low band) 950 - 2000 MHz (High band)	10.75 GHz (High band) 950 - 2100 MHz (Low band) 950 - 2000 MHz (High band)	11.3 GHz (Low band) 950 - 2100 MHz (Low band) 950 - 1450 MHz (High band)
Spurious signals -	60 dBm typ. @ 1000 MHz	-60 dBm typ. @ 1500 MHz	-60 dBm typ. @ 1300 MHz
SMW L	.O 10.75 & 11.3 GHz	LNA45.8	77.8 26.4
Frequency range Low/High 1	1.7 - 12.75 / 12.25 - 12.75 GHz		
LO frequency 1	0.75 GHz (Low band) 1.3 GHz (High band)		
Output frequency 9	050 - 2000 MHz (Low band)		
Spurious signals -	60 dBm typ. @ 1100 MHz		
Typical System specification (ma	ay vary with SMA cable length)		Sep. DC power sup (Changed to SMA-
Noise figure typical	1 0 dB		connector instead of the "pin")
Gain typ	52 dB +/- 4 dB within each b	and (See option)	
Gain variation typ	+/- 0.4 dB within 30 MHz	· · ·	511 <i>62</i> /
LO stability over temp.	+/- 25 kHz		and a second
LO Phase noise tvp.	-70 dBc @ 1 kHz		
	-85 dBc @ 5 kHz	Block Downconver	rtor (BDC)
	-90 dBc @ 10 kHz	BIOCK DOWIICOIIVEI	
	-110 dBc @ 100 kHz		Option Sep. DC power suppl
I O radiation	-120 dBc @ >1 MHz -60 dBm		
Image rejection	40 dB min.		
1 dB gain compression point	+5 dBm	0	
DC power LNA (sep.)	12-24V / 40 mA typ		
DC power for each Block Downcon	verter 12-24V / 250 mA typ		
Operating temperature	$-30\ t0\ +00^{\circ}C$		
Input flange I NA	WR-75 waveguide	- 10 KHZ)	•
Output LNA	SMA-connectors		
Input Block Downconverter	SMA-connectors		
Output connector Block Downconve	erter F-connector 75 ohm or		
	N-connector 50 ohm		
	Water-proof		
Dimensions I NA	2.0.1 max 81 x 40x 40 mm		
Weight LNA	124 a		
Dimensions Block Downconverter	191 x 63 x 50 mm		
Weight Block Downconverter	606 g (F-connector)		
	645 g (N-connector)		
Options	I O stability ±/- 10 kHz over t	he temp -10° to $\pm 70^{\circ}$ C	190
options	Customized gain (The total g	ain included the dish	
	shouldn't exceed 100 dB)		
	RF-shielding		
	Sep. DC power supply BDC	0 0	
	DC from one of the BDC's to		
	Specified LO. Min. 50-100 pc PLL with ext 10 MHz rof		
		9	
The system consists of:		, 0 0	
1 pc LNA Dual Output SMA			Ontion Sen DC nower supply
2 pcs SMA-cables lengh 4-6 m/each			Option Sep. DC power supply
2 pcs Block downconverters (BDC)			
	2	г	
<i>JNN/</i>	~		
			BDC Low (Sep. DC)



IF + DC (IF)

(Sep. DC)

IF+DC (IF)

BDC Low

BDC High

Low

High

LNA